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## FAX COVER SHEET

June 25, 2004

Page 1 of 3

<b>FROM:</b>  Steve Mendelsohn stevem@mendelip.com	<b>TO:</b>  Attn.: Filing Receipt Corrections
<b>COMPANY NAME:</b>  Mendelsohn & Associates, P.C.	<b>COMPANY NAME:</b>  USPTO
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## MESSAGE

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**PATENT**Lattice Ref.: L03-055Re: Attorney Docket No. 1054.030In re application of: Howard Tang, Satwant Singh, and Ann Wu

Serial No.: 10/809,658 Group No.: 2819  
 Filed: 03/25/04 Examiner: TBD

For: Configuring FPGAs and the Like Using One or More Serial Memory Devices**TRANSMITTAL OF ERRORS IN FILING RECEIPT**

Commissioner for Patents  
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 Alexandria, VA 22313-1450

Accompanying this transmittal is a copy of the Filing Receipt for the above-identified matter. The Filing Receipt contains the following error(s) and the corrections are as follows:

1. In the title, please replace "Configuring" with -Configuring-.

Changes are noted on the accompanying copy of the Filing Receipt.

Respectfully submitted,

Date: 6/25/04  
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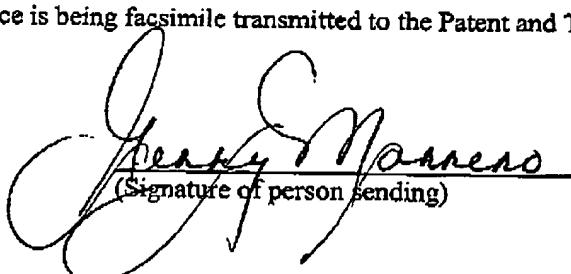
\* \* \* \*

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Date of Deposit 6/25/04

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Gerry Marrero  
 (Name of person sending)

  
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO.	DRAWINGS	TOT CLMS	IND CLMS
10/809,658	03/25/2004	2819	1298	1054.030	7	35	6

22186  
MENDELSONN AND ASSOCIATES PC  
1515 MARKET STREET  
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CONFIRMATION NO. 7489  
FILING RECEIPT



"OC000000012926015"

Date Mailed: 06/14/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Howard Tang, Cupertino, CA;  
Satwant Singh, Fremont, CA;  
Ann Wu, San Jose, CA;

**Assignment For Published Patent Application**

Lattice Semiconductor Corporation, Hillsboro, OR;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

If Required, Foreign Filing License Granted: 06/10/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

**Configuring**

Configuring FPGAs and the like using one or more serial memory devices